

Application Note 00201

In-Circuit Programming of Cypress ISR devices

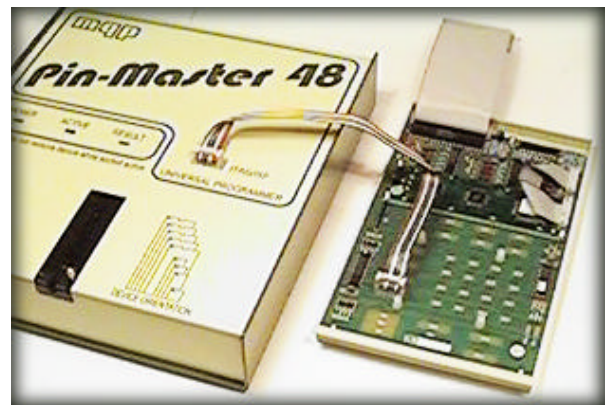
This application note describes how Cypress ISR devices may be programmed in-circuit using the dedicated JTAG connector on the Pin-Master 48 universal programmer. The programming cable and programming procedures are described.

INTRODUCTION

An increasing number of devices have an in circuit programming capability, allowing the devices to be programmed while soldered onto a system board.

The programming data is transmitted and received over a serial interface. On many devices this interface conforms to the JTAG (IEEE Std 1149.1) standard or in some cases to a proprietary standard.

The Pin-Master 48 universal programmer has a dedicated JTAG connector fitted to the top of the unit. A cable made up to the users own requirements connects the socket to the target board.



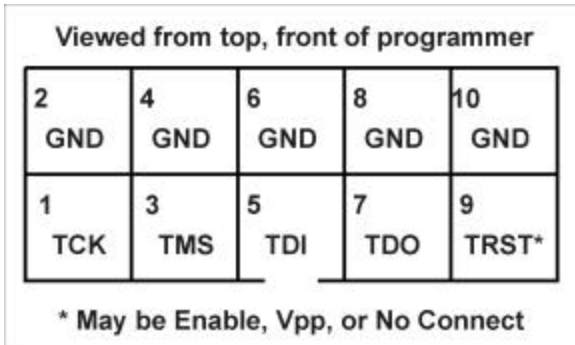
Cypress' Ultra37000 series parts can very easily be programmed using this JTAG interface. The simplest way is for the target board to be laid out to match the Pin-Master 48 JTAG connector.

Alternatively the designer may have chosen to use the Cypress recommended pin-out. This application note describes how to interconnect the Pin-Master 48 and the ISR connector.

The JTAG method can also be extended to allow the programming of a chain of JTAG devices.

PIN-MASTER 48 JTAG SOCKET

The socket is a standard 10 way 0.1" header. A diagram of the socket is shown below:

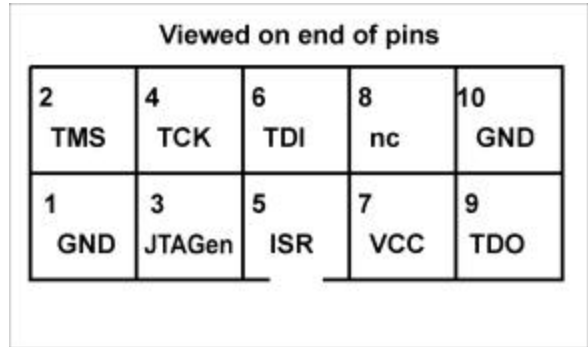


1	TCK	Test Clock	Device input
2	GND	Ground	
3	TMS	Test Mode Select	Device input
4	GND	Ground	
5	TDI	Test Data Input	Device input
6	GND	Ground	
7	TDO	Test Data Output	Device output
8	GND	Ground	
9	TRST*	Test Reset	Device input
10	GND	Ground	

* TRST is an optional signal on a JTAG interface. On some devices it is used for an ENABLE or Vpp high voltage signal. On Cypress Ultra37000 devices it is the JTAGen signal.

CYPRESS ISR CONNECTOR

The socket is a standard 10 way 0.1" header. A diagram of the plug is shown below:



1	GND	Ground	
2	TMS	Test Mode Select	Device input
3	JTAGen	Enable JTAG	Device input
4	TCK	Test Clock	Device input
5	ISR	Low for ISR operation	Input to target board
6	TDI	Test Data Input	Device input
7	VCC		Output from target board
8	nc	-	
9	TDO	Test Data Output	Device output
10	GND	Ground	

CABLE

A ribbon cable with alternate wires connected to ground is used between the Pin-Master 48 and the target board. The cable should be kept as short as possible (maximum length 1.5m).

Signal Name	Pin-Master 48	ISR	
TCK	1	4	
TMS	3	2	
TDI	5	6	
TDO	7	9	
JTAGen	9	3	
GND	2, 4, 6, 8, 10	1, 10, 5 (ISR)	
VCC	nc	7	

The re-arrangement of signals necessary should occur at the ISR end of the cable to preserve the transmission line effect of the alternate GND signals.

We recommend that the grounds should all be connected together at the ISR end of the cable and then joined to the ISR ground.

TARGET BOARD

The target board should be equipped with a suitable connector. This could be most simply achieved with a connector and pin layout similar to that used at the programmer end of the cable.

For target boards designed to Cypress ISR specification the connections shown above should be followed.

A JTAG interface implies the use of internal pull up resistors on input pins. The user would be advised to check the device specification before deciding if external resistors are required.

Please note that during programming all normal outputs on the device will be tri-stated. The design of the target board should be such that this does not cause any problems to any other circuitry.

PREPARING FILES

The current Pin-Master 48 support for Cypress Ultra37000 CPLD's is by the use of JAM format files. After using Warp2¹ to generate the JEDEC file for each device in the JTAG chain, use ISR to prepare the JAM file. This allows you to generate a JAM file which can program and verify one or more Cypress devices in the JTAG chain.

When using PROMDRIVER with the Pin-Master 48, simply open the JAM file and click on the PROG button.

Please note that the JAM files generated by ISR 2.2 do not contain the commands required to allow selection of individual functions such as VERIFY, BLANKCHECK or ERASE. Pressing any of the available keys will simply play the JAM file and perform the pre-defined functions contained in it.

PROGRAMMING PROCEDURE

As no power is provided by the programmer, the target board must be independently powered.

To avoid earth potential differences the programmer and target board should be connected to a common mains supply.

- Connect the cable to the target board and the programmer.
- Power up the target board and proceed with programming in the normal way.

¹ Warp2 and ISR are applications available from Cypress Semiconductor Corporation



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